

Replacement Sheet

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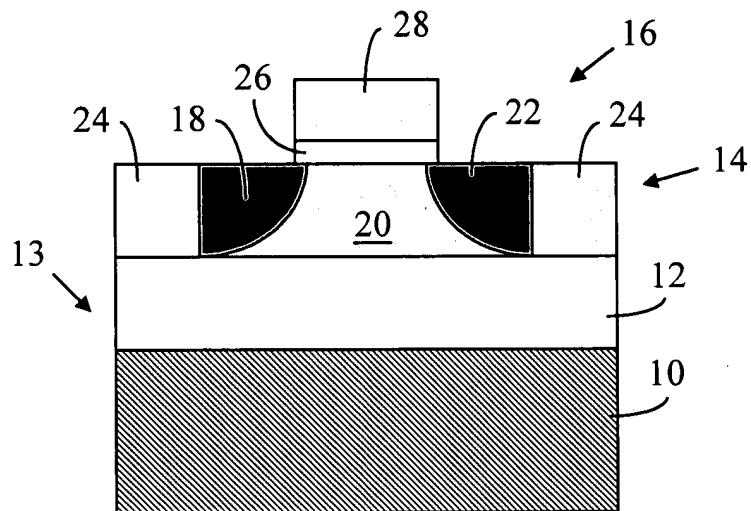


Fig. 1

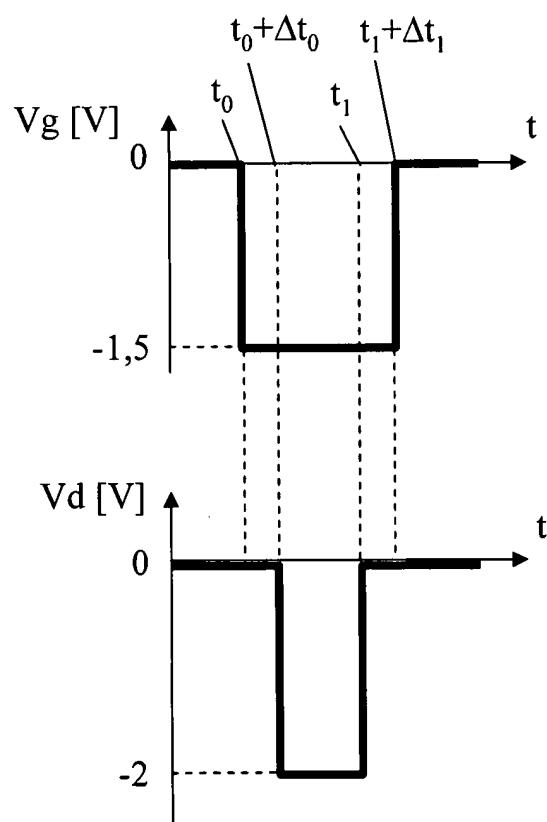


Fig. 2

# Replacement Sheet

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Fig. 5b

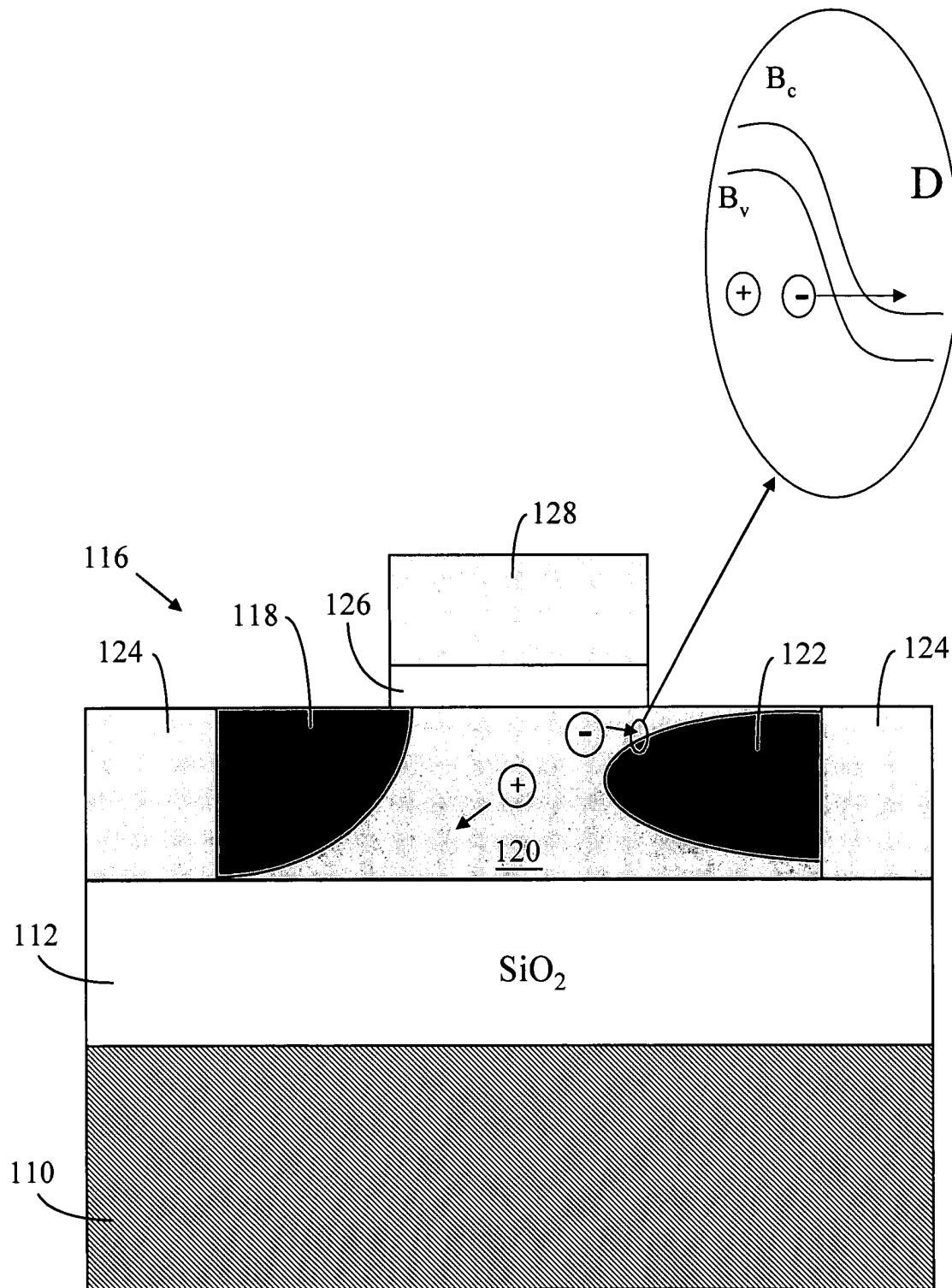


Fig. 5a

# Replacement Sheet

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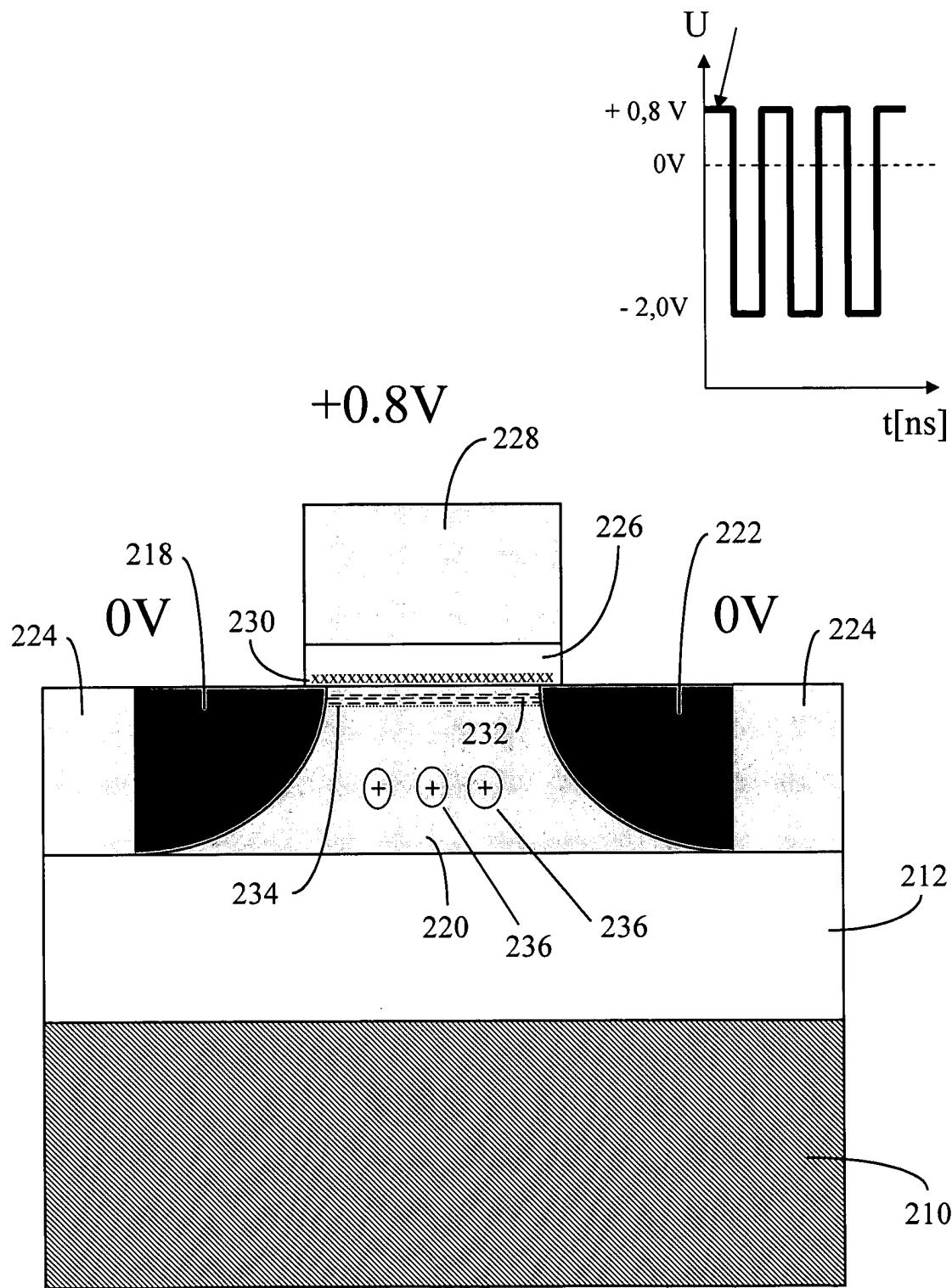


Fig. 6a

# Replacement Sheet

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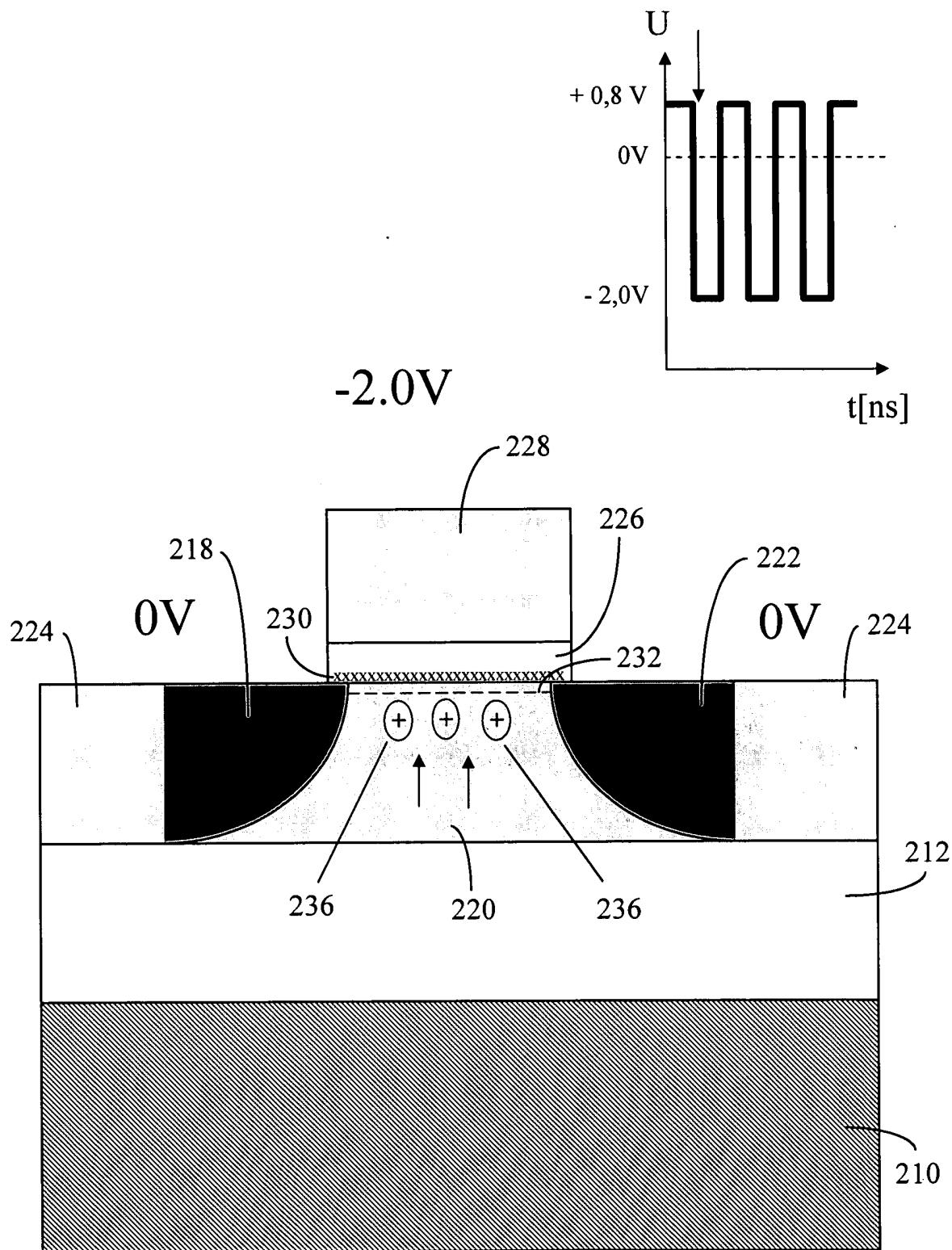


Fig. 6b

# Replacement Sheet

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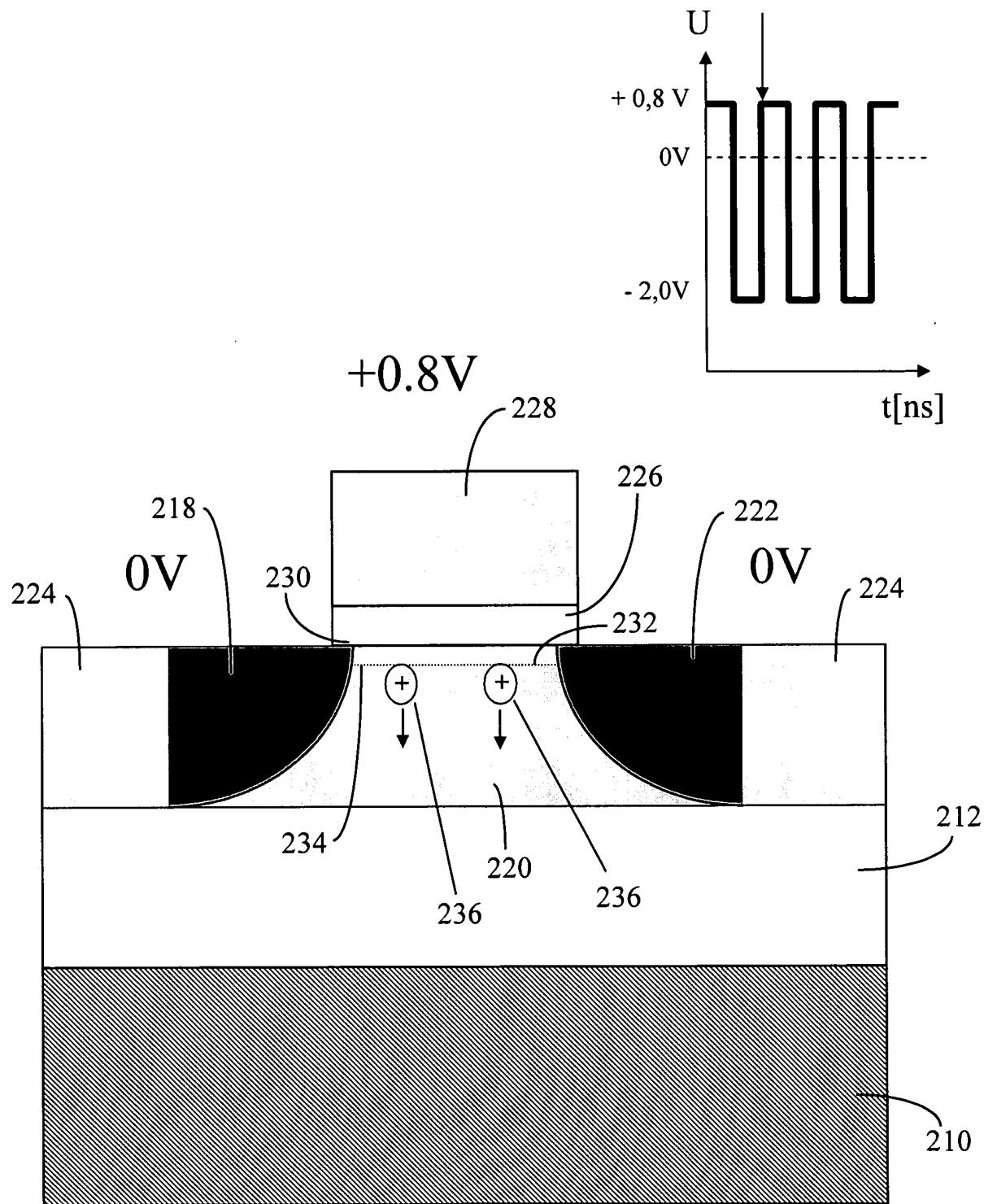


Fig. 6c

# Replacement Sheet

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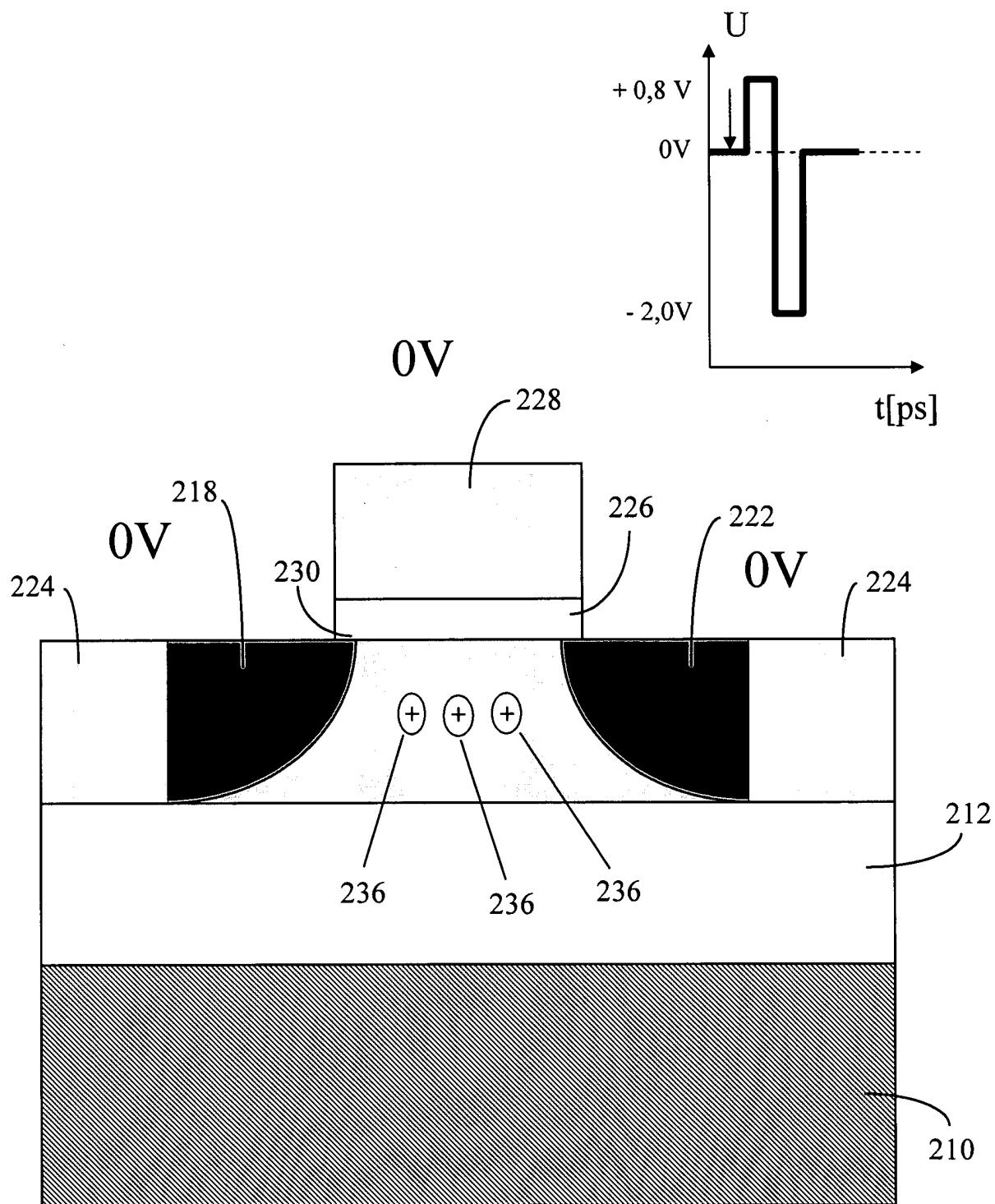


Fig. 7a

# Replacement Sheet

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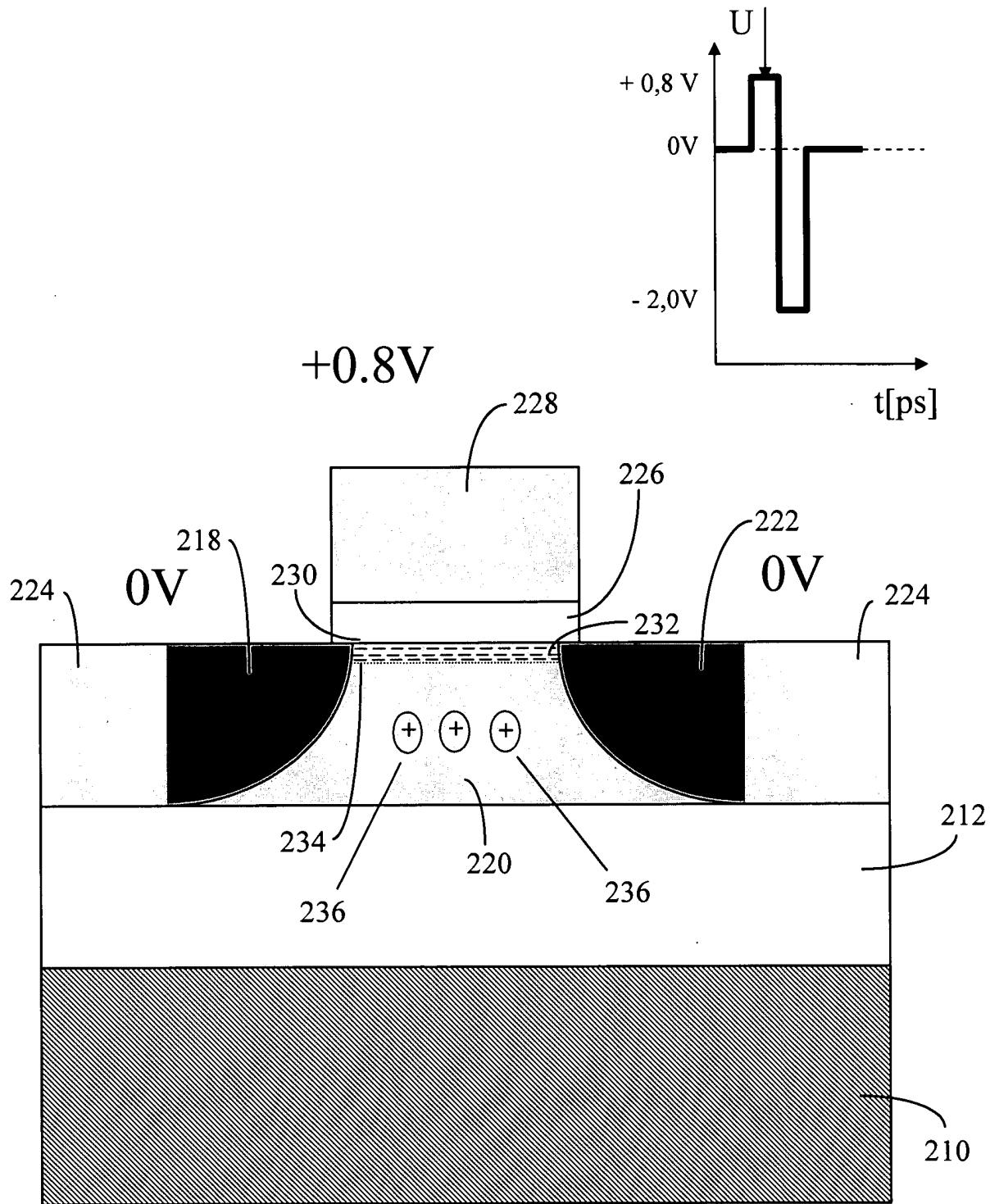


Fig. 7b

# Replacement Sheet

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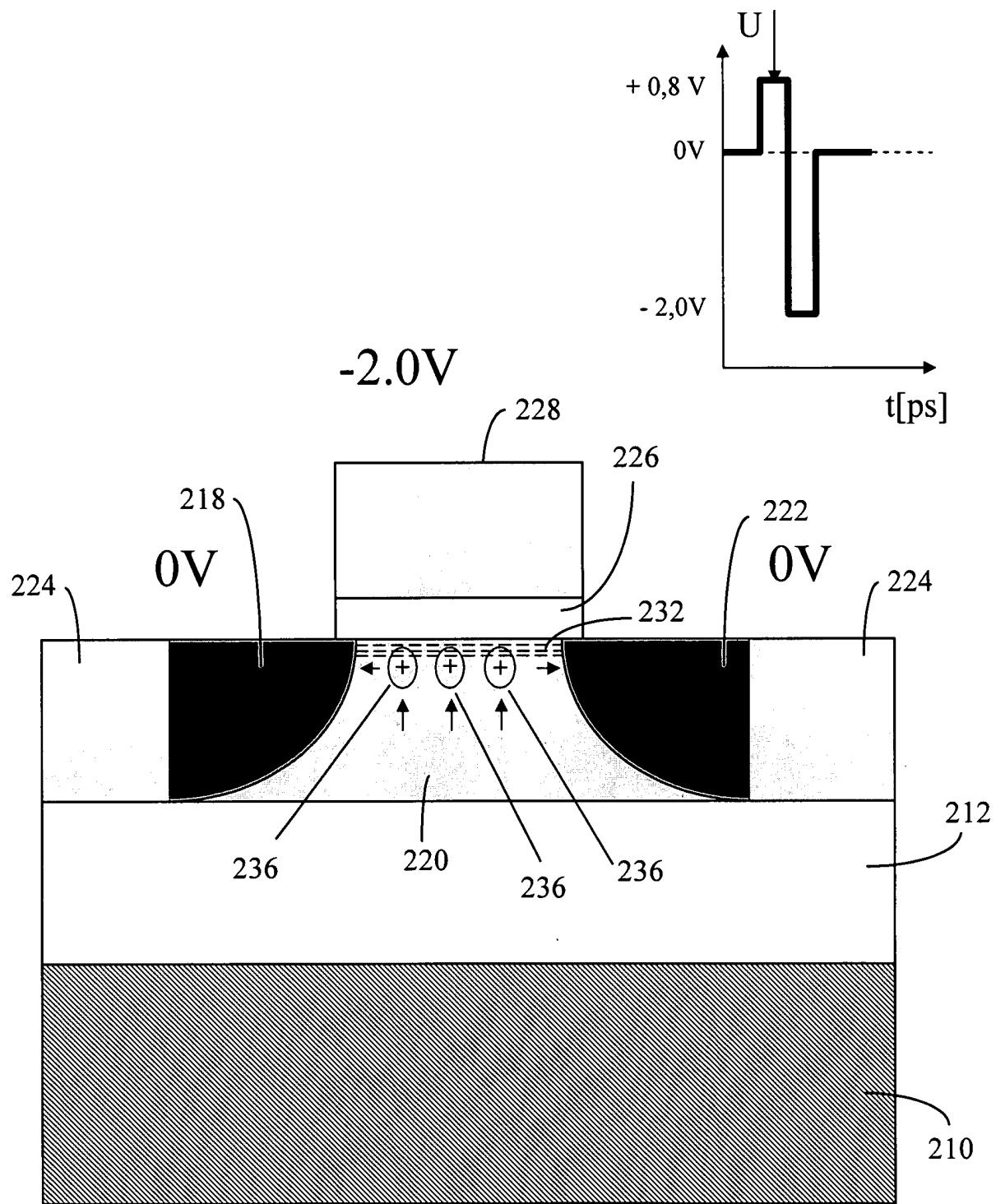


Fig. 7c

# Replacement Sheet

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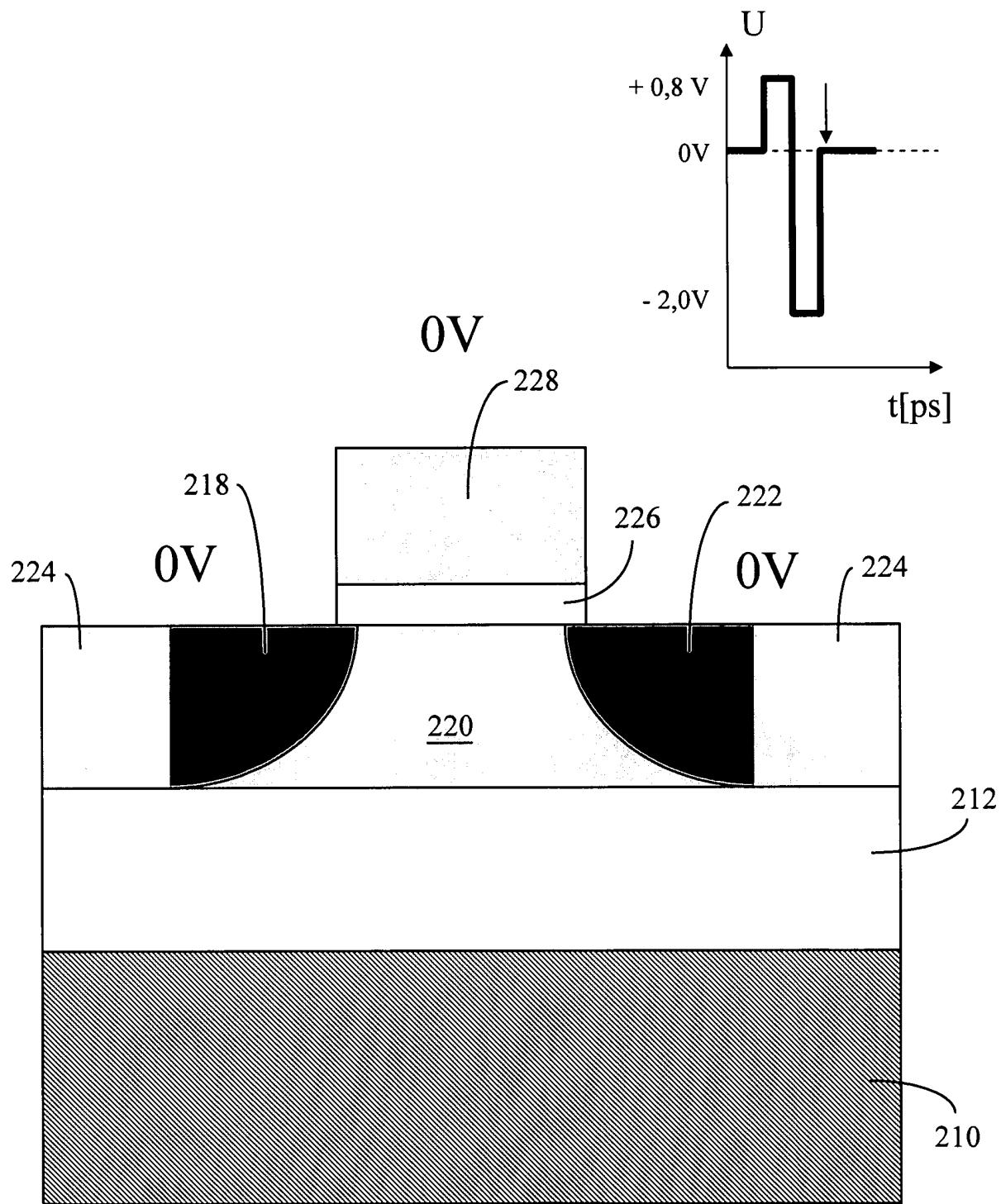


Fig. 7d

# Replacement Sheet

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## PD-SOI NMOS

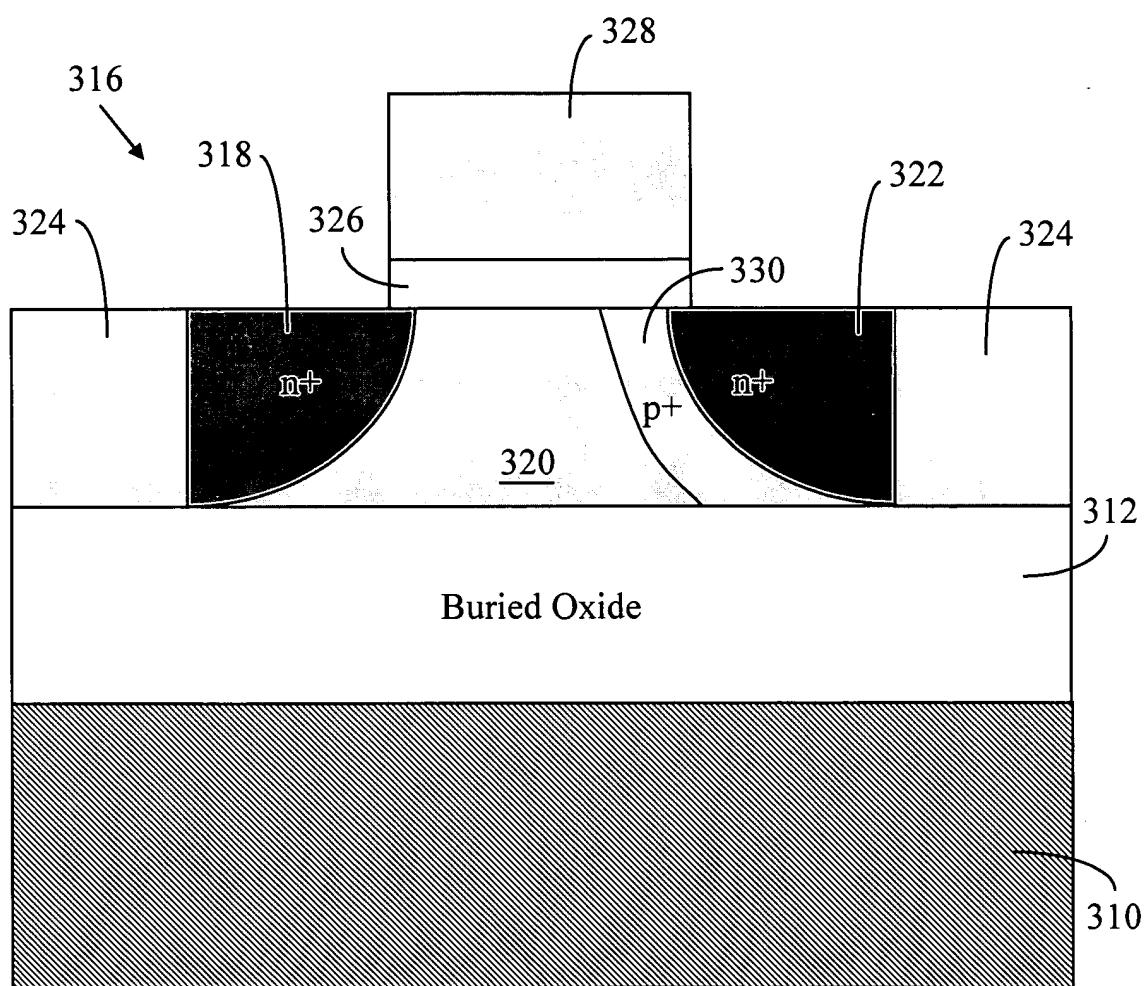
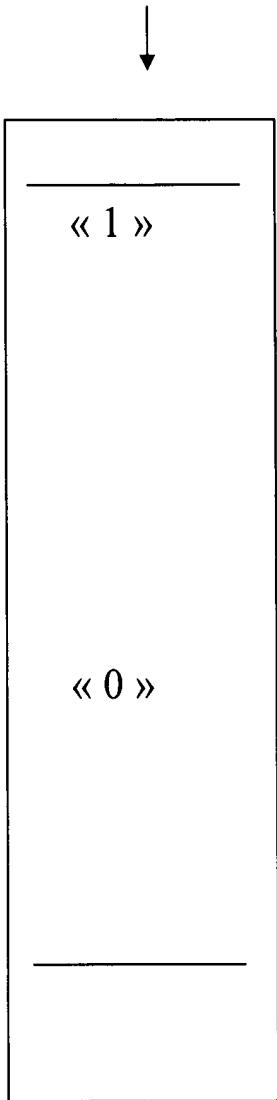


Fig. 8

# Replacement Sheet

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Binary memory



Multilevel memory

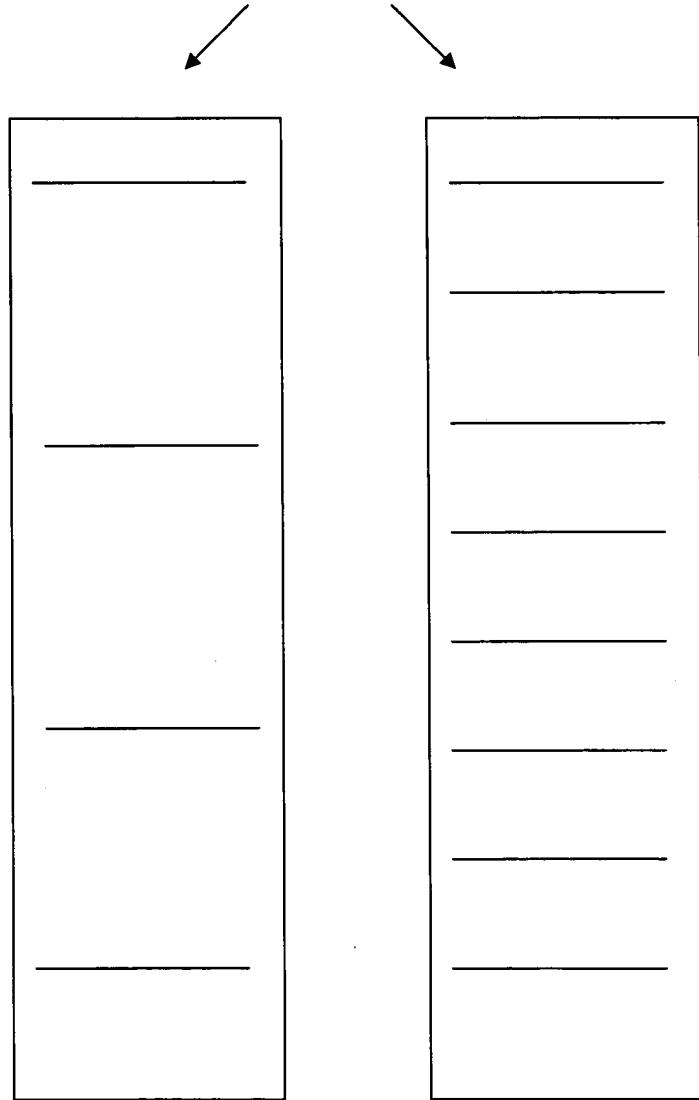


Fig. 10a

Fig. 10b

Fig. 10c

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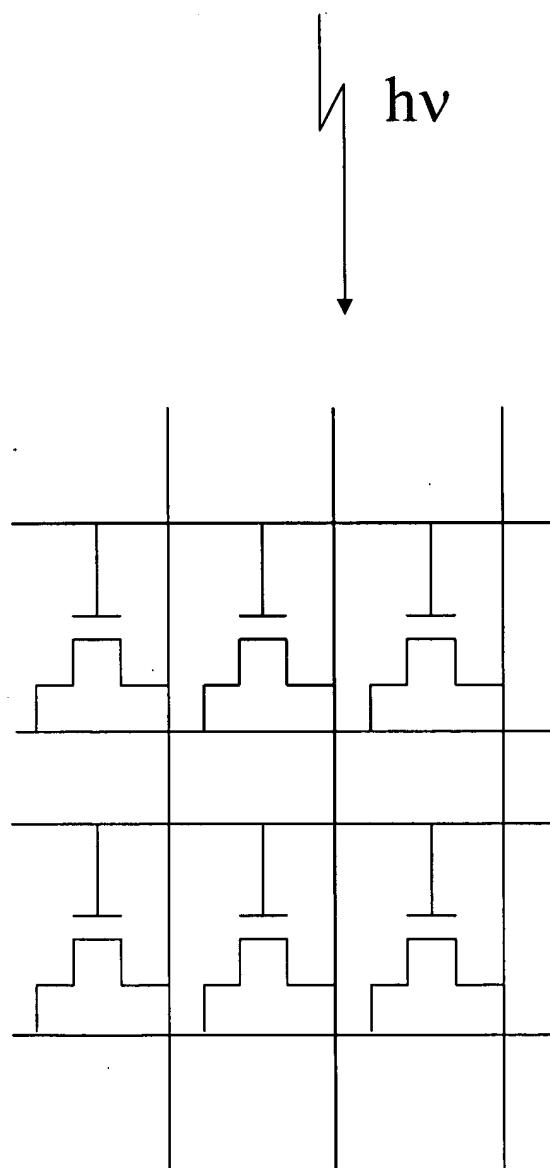


Fig. 18